

N THE BOOK STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Soichi Kobayashi et al.

Group Art Unit: 2138

Application No.: 10/632,928

Examiner: Saquib J. Siddiqui

Filing Date:

August 4, 2003

Confirmation No.: 4942

Title: SEMICONDUCTOR INTEGRATED CIRCUIT CAPABLE OF TESTING WITH SMALL SCALE CIRCUIT

CONFIGURATION

PETITION FOR EXTENSION OF TIME

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

The following extension of time is requested to: the Office Action dated December 29, 2005

	one month to	April 29,	2006	\$60.00 (2251)	× \$120.00 (1251)
☐ The shortened statutory period has been reset by an Advisory Action dated					
	An extension	fee in the amo	unt of	is end	closed.
	Charge		to Deposit Acc	ount No. 02-4800).
X	Charge	\$ 120.00	to credit card.	Form PTO-2038	is attached.
The Director is hereby authorized to charge any appropriate fees under 37 C.F.R. §§1.16, 1.17 and 1.21 that may be required by this paper, and to credit any overpayment, to Deposit Account No. 02-4800. This paper is submitted in duplicate.					
				Respectfully sul	bmitted,
				BUCHANAN IN	GERSOLL PC
P.O. Box 1404 Alexandria, Virginia 22313-1404 (703) 836-6620				Ву	
Date: A	April 28, 2006			Ellen Marc Registratio	n No. 32,131

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